

<b>Notice of References Cited</b>	Application/Control No.	Applicant(s)/Patent Under Reexamination	
	10/509,753	YAMAWAKI ET AL.	
Examiner	Junpeng Chen	Art Unit	2618

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-4,501,002 A	02-1985	Auchterlonie, Richard C.	375/332
*	B	US-7,053,723 B2	05-2006	Fredriksson, Jesper	331/135
*	C	US-2006/0166619 A1	07-2006	Roberts, Richard D.	455/039
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Buchwald, "A 6-GHz integrated phase-locked loop using AlGaAs/GaAs heterojunction bipolar transistors", Volume: 27 Issue: Dec. 12 1992, pages: 1752-1762
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.